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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Pascal Bolcato and Gerard Eichenmueller

Application No. Not yet assigned**Filed:** Herewith**Confirmation No.** Not yet assigned**For:** ACCELERATED ANALOG AND/OR RF
SIMULATION**Examiner:** Not yet assigned**Art Unit:** Not yet assigned**Attorney Reference No.** 7126-72225-01

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INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 C.F.R. § 1.97(b)(1)

Listed on the accompanying form PTO-1449 and enclosed herewith are several English-language documents. Applicants respectfully request that these documents be listed as references cited on the issued patent.

Copies of United States patents and United States published patent applications do not have to be provided to the Patent Office (37 C.F.R. 1.98(a)(2)(ii)). Copies of unpublished U.S. applications do not have to be provided, as long as the application is available on PAIR, as this requirement of 37 C.F.R. § 1.98(a)(2)(iii) has been waived by the United States Patent and Trademark Office pursuant to the Official Gazette Notice on October 19, 2004 (1287 OG 163). Applicants will provide copies of such patents or applications upon request.

Applicants filed this Information Disclosure Statement ("IDS") within three months of the filing date of a national application. As a result, no fee should be required to file this IDS. However, if the Patent Office determines that a fee is required for Applicants to file this IDS, please charge any such fees, or credit overpayment, to Deposit Account No. 02-4550.

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The filing of this IDS shall not be construed to be an admission that the information cited in the statement is, or is considered to be, prior art or otherwise material to patentability as defined in 37 C.F.R. §1.56.

Respectfully submitted,

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| | | First Named Inventor | Pascal Bolcato |
| | | Art Unit | Not yet assigned |
| | | Examiner Name | Not yet assigned |

U.S. PATENT DOCUMENTS

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

| Examiner's Initials* | Cite No. (optional) | Number | Publication Date | Name of Applicant or Patentee |
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FOREIGN PATENT DOCUMENTS

| Examiner's Initials* | Cite No. (optional) | Country | Number | Publication Date | Name of Applicant or Patentee |
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OTHER DOCUMENTS

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| | Feldmann, P. et al., "Computation of circuit waveform envelopes using an efficient, matrix-decomposed harmonic balance algorithm," <i>International Conference on Computer-Aided Design</i> , pp. 295-300 (1996). |
| | Ho, C. et al., "The Modified Nodal Approach to Network Analysis," <i>IEEE Transactions on Circuits and Systems</i> , Vol. CAS-22, No. 6, pp. 504-509 (June 1975). |
| | Ngoya, E. et al., "Envelop Transient Analysis: A New Method for the Transient and Steady State Analysis of Microwave Communication Circuits and Systems," <i>IEEE MTT-S Digest</i> , pp. 1365-1368 (1996). |
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| EXAMINER SIGNATURE: | DATE CONSIDERED: |
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* Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.